

Electronic Acknowledgement Receipt

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Confirmation Number:	2398
Title of Invention:	Circuit arrangement and signal processing device
First Named Inventor/Applicant Name:	Werner Hemmert
Customer Number:	25281
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
1	Information Disclosure Statement (IDS) Filed	432118101_IDS_3.pdf	706767 b27d43b782c88f43c9720b1757d4325a e13c654f	no	4

Warnings:

Information:

2	Foreign Reference	JP57135595.pdf	293798 6606c39654484adaf14e84f5e616bb5cf b31836a	no	4
Warnings:					
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3	Foreign Reference	JP58223909.pdf	1005561 035607478c6d1a2ae87b20da057abc8 82843c883	no	9
Warnings:					
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4	Foreign Reference	JP5939529.pdf	124291 9a223767bc73382c15b0a2f5285fb704 68d9a1fc	no	2
Warnings:					
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5	Foreign Reference	JP2001217973.pdf	1313188 13e6335e778268e4de1e82361340405 2a8cfd9c5	no	10
Warnings:					
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6	NPL Documents	JP_OA.pdf	175693 b1c8b04d828a2b4396752a2ab7560cd 3cc6319e7	no	2
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